UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

25537

7590

12/07/2009

VERIZON
PATENT MANAGEMENT GROUP
1320 North Court House Road
9th Floor
ARLINGTON, VA 22201-2909

EXAMINER				
DENG, ANNA CHEN				
ART UNIT	PAPER NUMBER			

2191

DATE MAILED: 12/07/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/797,068	03/11/2004	Yih-Feng Hwang	ASH-03-010	4377

TITLE OF INVENTION: INTEGRATION OF INFORMATION DISTRIBUTION SYSTEMS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/08/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or <u>Fax</u> (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where m

appropriate. All further indicated unless correct maintenance fee notifica	ed below or directed otl	ng the Patent, advance o herwise in Block 1, by (orders and notification of a) specifying a new co	of m orresp	aintenance fees woondence address;	ill be and/or	mailed to the current (b) indicating a sepa	correspondence address a rate "FEE ADDRESS" fo
CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)				Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.				
25537 7590 12/07/2009 VERIZON PATENT MANAGEMENT GROUP 1320 North Court House Road					Cer	tificate	of Mailing or Transı	nission deposited with the Unitec t class mail in an envelope above, or being facsimile tte indicated below.
9th Floor			ſ					(Depositor's name)
ARLINGTON,	VA 22201-2909		ļ					(Signature)
								(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENT	OR		ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
10/797,068 TITLE OF INVENTION	03/11/2004 I: INTEGRATION OF II	NFORMATION DISTRI	Yih-Feng Hwang BUTION SYSTEMS				ASH-03-010	4377
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DU	UE	PREV. PAID ISSU	E FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300		\$0		\$1810	03/08/2010
EXAM	IINER	ART UNIT	CLASS-SUBCLASS	s				
DENG, AN	INA CHEN	2191	717-120000					
"Fee Address" ind PTO/SB/47; Rev 03-(Number is required. 3. ASSIGNEE NAME A PLEASE NOTE: Uni	ND RESIDENCE DATA less an assignee is ident h in 37 CFR 3.11. Com	" Indication form ned. Use of a Customer A TO BE PRINTED ON	data will appear on th	native ingle or ag attorn be p t type e pal an a	firm (having as a gent) and the nam news or agents. If wrinted.	memb es of uj no nam	er a 2ot o e is 3	ocument has been filed for
Please check the appropr	riate assignee category or	r categories (will not be p	rinted on the patent):		Individual 🖵 Co	orporati	on or other private gro	up entity 🗖 Government
4a. The following fee(s) are submitted: ☐ Issue Fee ☐ Publication Fee (No small entity discount permitted) ☐ Advance Order - # of Copies			 4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) A check is enclosed. Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number (enclose an extra copy of this form). 					
5. Change in Entity Sta	tus (from status indicate as SMALL ENTITY state	*	☐ b. Applicant is no	long	er claiming SMAl	LL EN	TITY status. See 37 CF	TR 1.27(g)(2).
NOTE: The Issue Fee an interest as shown by the	d Publication Fee (if req records of the United Sta	uired) will not be accepte ates Patent and Trademark	ed from anyone other that k Office.	an th	e applicant; a regi	stered a	attorney or agent; or th	e assignee or other party in
Authorized Signature					Date			
Typed or printed name			Registration No					
This collection of inform an application. Confiden submitting the complete this form and/or suggest Box 1450, Alexandria, V Alexandria, Virginia 223	tiality is governed by 35 d application form to the ions for reducing this bu /irginia 22313-1450. DO	CFR 1.311. The information of U.S.C. 122 and 37 CFR EUSPTO. Time will varyurden, should be sent to the ONOT SEND FEES OR	on is required to obtain 1.14. This collection is y depending upon the ir the Chief Information Of COMPLETED FORMS	or re s estindivi- fficer S TO	etain a benefit by t mated to take 12 i dual case. Any co r, U.S. Patent and THIS ADDRESS	he publ minutes mment Traden S. SENI	ic which is to file (and to complete, including s on the amount of tin nark Office, U.S. Depa O TO: Commissioner f	by the USPTO to process g gathering, preparing, and ee you require to complet rtment of Commerce, P.O or Patents, P.O. Box 1450

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

OMB 0651-0033

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS

P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

O. CONFIRMATION NO.
4377
AMINER
ANNA CHEN
PAPER NUMBER
_
2009

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 740 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 740 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)				
	10/797,068	HWANG, YIH-FENG				
Notice of Allowability	Examiner	Art Unit				
	ANNA DENG	2191				
	ANNA DENG	2191				
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comi IGHTS. This application is	in this application. If not included nunication will be mailed in due course. THIS				
1. This communication is responsive to <u>9/17/2009</u> .						
2. \boxtimes The allowed claim(s) is/are <u>1, 2, 6-10, 14-23, 27, and 28,</u> .						
 3. ☐ Acknowledgment is made of a claim for foreign priority u a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 	e been received.	, , , , , , , , , , , , , , , , , , ,				
Copies of the certified copies of the priority do	• •					
International Bureau (PCT Rule 17.2(a)).						
* Certified copies not received:						
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.						
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv						
5. CORRECTED DRAWINGS (as "replacement sheets") must	st be submitted.					
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached						
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date						
(b) ☐ including changes required by the attached Examiner Paper No./Mail Date	s Amendment / Comment	or in the Office action of				
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the						
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.						
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. Notice of	Informal Patent Application				
2. \square Notice of Draftperson's Patent Drawing Review (PTO-948)		Summary (PTO-413),				
3. Information Disclosure Statements (PTO/SB/08),	Paper No 7. ⊠ Examiner	o./Mail Date 's Amendment/Comment				
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit 8. Examiner's Statement of Reasons for Allowa						
of Biological Material	9. 🔲 Other	<u>_</u> .				
/Anna Deng/						
Primary Examiner, Art Unit 2191						

Art Unit: 2191

DETAILED ACTION

1. This action is in response to amendment filed on 9/17/2009.

- 2. The rejection under 35 U.S.C. 101 to claims 9-16, and 22-28 is withdrawn in view of applicant's amendment.
- 3. The rejection under 35 U.S.C. 103(a) as being unpatentable over Chan et al. (USPN 6910,028 B2), in view of applicant submitted IDS, Hwang et al. et al. "Detecting Faults In Chained-Inference Rules In Information Distribution Systems" (Summer, 1997) to claims 1-4, 6-12, 14-25, and 27-28 is withdrawn in view of applicant's amendment.
- 4. The rejection under 35 U.S.C. 103(a) as being unpatentable over Chan et al. (USPN 6910,028 B2), in view of applicant submitted IDS, Hwang et al. et al. "Detecting Faults In Chained-Inference Rules In Information Distribution Systems" (Summer, 1997), further in view of Bahrami, (USPUB 2004/0078777 A1) to claims 5, 13, and 26 is withdrawn in view of applicant's amendment.
- 5. Claims 1, 6, 9, 14, 17, and 22 have been amended (see Examiner's Amendment below).
- 6. Claims 3-5, 11-13, and 24-26 have been canceled (see Examiner's Amendment below).
- 7. Claims 1-2, 6-10, 14-23, and 27-28 are pending.
- 8. Claims 1-2, 6-10, 14-23, and 27-28 are allowed.

EXAMINER'S AMENDMENT

9. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with James Bennin (Reg. No. 60,692) on 11/30/2009 to place the application in condition of allowance.

The application has been amended as follows:

In the Claims:

Claims 3-5, 11-13, and 24-26 have been canceled.

Claims 1, 6, 9, 14, 17, and 22 have been amended as following:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A computer-implemented method of integrating software systems, the method comprising:

identifying, by a processor of \underline{a} the computer, the software systems that are to be integrated;

Application/Control Number: 10/797,068

a first level that includes the software systems,

Art Unit: 2191

identifying, by the a processor of the computer, a scope of the integration based on a multi-level top-down approach, where the multi-level top-down approach includes:

a second level that includes business processes of the software systems,
a third level that includes business rules that are defined as transitions in
the business processes,

Page 4

a fourth level that includes interface functions that define communications between the business rules, and

a fifth level that includes data used by the business rules and the interface functions;

identifying, by the processor <u>of the computer</u>, <u>the business processes</u>, associated with the software-<u>system systems</u>, that are to be integrated;

identifying, by the processor <u>of the computer</u>, <u>the business rules that are defined</u> for each of the business processes that are to be integrated;

identifying, by the processor <u>of the computer</u>, faults in the business rules by applying generic depth-first search (DFS)-based techniques to the business rules; and modifying, by the processor <u>of the computer</u>, the business rules based on the identified faults.

2. (previously presented) The computer-implemented method of claim 1, where identifying faults in the business rules includes:

Art Unit: 2191

representing the business rules using a transition-directed graph (TDG) representation.

3.-5. (canceled)

6. (currently amended) The computer-implemented method of claim 1[[4]], further including:

comparing the business processes to locate similar business processes that are to be integrated.

- 7. (previously presented) The computer-implemented method of claim 1, where identifying the scope of the integration is performed on software systems from multiple merging entities.
- 8. (previously presented) The computer-implemented method of claim 1, where the identified faults include faults of at least one of inconsistency, contradiction, circularity, subsumption, redundancy, or incompleteness.
- 9. (currently amended) A computer-implemented system for integrating information distribution systems, the computer-implemented system comprising:
 - a physical memory device to store instructions; and
 - a processor to execute the instructions to implement:

Art Unit: 2191

means for identifying the information distribution systems that are to be integrated;

means for assisting a user to identify a scope of the integration using a multi-level top-down approach, the identified scope including a set of business processes that are to be integrated and a set of business rules that define the business processes, where the multi-level top-down approach includes:

a first level that includes software systems,

<u>a second level that includes the business processes, which define</u>
the software systems,

<u>a third level that includes the business rules defined as transitions</u> <u>in the business processes,</u>

<u>a fourth level that includes interface functions that define</u>
<u>communications between the business rules, and</u>

<u>a fifth level that includes data used by the business rules and the</u>
<u>interface functions</u>; and

means for identifying faults in the business rules by applying generic depth-first search (DFS)-based techniques to the business rules; and means for modifying the business rules based on the identified faults.

10. (previously presented) The computer-implemented system of claim 9, where the fault detection component is further configured to represent the business rules using a transition-directed graph (TDG) representation.

Art Unit: 2191

11.-13. (canceled)

- 14. (currently amended) The computer-implemented system of claim <u>9</u>[[12]], where the means for assisting compares the business processes to locate similar business processes that are to be integrated.
- 15. (previously presented) The computer-implemented system of claim 9, where the scope of the integration is defined for software systems from multiple merging entities.
- 16. (previously presented) The computer-implemented system of claim 9, where the identified faults include faults of at least one of inconsistency, contradiction, circularity, subsumption, redundancy, or incompleteness.
- 17. (currently amended) A computer-implemented method of integrating information distribution systems of merging entities, the method comprising:

identifying, by a processor of <u>a</u> the computer, top-level software a scope of the <u>information distribution</u> systems that are to be integrated <u>based on a multi-level top-down approach including:</u>

a first level that includes software systems,

a second level that includes business processes, which define the software systems,

a third level that includes business rules defined as transitions in the business processes,

a fourth level that includes interface functions that define communications between the business rules, and

<u>a fifth level that includes data used by the business rules and the interface</u> functions;

identifying, by the processor <u>of the computer</u>, <u>the business processes in the top-level-software systems</u>;

comparing, by the processor <u>of the computer</u>, the identified business processes to determine business processes that are related as candidates for integration;

identifying, by the processor <u>of the computer</u>, <u>the business rules that define the identified business processes</u>; and

identifying, by the processor of the computer, faults in the business rules by applying generic depth-first search (DFS)-based techniques to the business rules; and modifying, by the processor of the computer, the business rules based on the identified faults.

18. (previously presented) The computer-implemented method of claim 17, further comprising:

modifying the business rules based on the identified faults.

Art Unit: 2191

19. (previously presented) The computer-implemented method of claim 17, where comparing the identified business processes includes finding pairs of business processes that perform similar functions.

- 20. (previously presented) The computer-implemented method of claim 17, where the identified faults include faults of at least one of inconsistency, contradiction, circularity, subsumption, redundancy, or incompleteness.
- 21. (previously presented) The computer-implemented method of claim 17, where identifying faults in the business rules further includes:

representing the business rules using a transition-directed graph (TDG) representation.

22. (currently amended) A physical computer-readable memory device containing instructions for execution by one or more processors, the physical computer-readable memory device including:

instructions for assisting a user to identify a scope of an integration of information distribution systems by using a multi-level top-down approach, the identified scope including a set of business processes that are to be integrated and a set of business rules that define the business processes, where the multi-level top-down approach includes:

Art Unit: 2191

a first level that includes software systems,

a second level that includes the business processes, which define the software systems,

a third level that includes the business rules defined as transitions in the business processes,

<u>a fourth level that includes interface functions that define communications</u> <u>between the business rules, and</u>

a fifth level that includes data used by the business rules and the interface functions; and

instructions for identifying faults in the business rules by applying generic depthfirst search (DFS)-based techniques to the business rules; and

instructions for modifying the business rules based on the identified faults.

23. (previously presented) The physical computer-readable memory device of claim 22, where the instructions for identifying faults represent the business rules using a transition-directed graph (TDG) representation.

24.-27. (canceled)

27. (previously presented) The physical computer-readable memory device of claim 22, where the scope of the integration is defined for information distribution systems from multiple merging entities.

Art Unit: 2191

28. (previously presented) The physical computer-readable memory device of claim 22, where the identified faults include faults of at least one of inconsistency, contradiction, circularity, subsumption, or incompleteness.

REASONS FOR ALLOWANCE

10. The following is an examiner's statement of reasons for allowance:

The cited prior art taken alone or in combination fail to teach, in combination with the other claimed limitations, identifying a scope of the integration based on a multi-level top-down approach, where the multi-level top-down approach includes: a first level that includes the software systems, a second level that includes business processes of the software systems, a third level that includes business rules that are defined as transitions in the business processes, a fourth level that includes interface functions that define communications between the business rules, and a fifth level that includes data used by the business rules and the interface functions; identifying the business processes associated with the software systems that are to be integrated; identifying the business rules that are defined for each of the business processes that are to be integrated; identifying faults in the business rules by applying generic depth-first search (DFS)-based techniques to the business rules; and modifying the business rules based on the identified faults as recites in independent claim 1, and further fails to teach similarly worded limitations in independent claims 9, 17, and 22.

Art Unit: 2191

The closes cited prior arts, the combination of Chan et al. (USPN 6910,028 B2), Hwang et al. et al. "Detecting Faults In Chained-Inference Rules In Information Distribution Systems" (Summer, 1997), and Bahrami, (USPUB 2004/0078777 A1) teaches a method of integrating software systems. However, the combination of Chan et al., Hwang et al., and Bahrami fails to teach identifying a scope of the integration based on a multi-level top-down approach, where the multi-level top-down approach includes: a first level that includes the software systems, a second level that includes business processes of the software systems, a third level that includes business rules that are defined as transitions in the business processes, a fourth level that includes interface functions that define communications between the business rules, and a fifth level that includes data used by the business rules and the interface functions; identifying the business processes associated with the software systems that are to be integrated; identifying the business rules that are defined for each of the business processes that are to be integrated; identifying faults in the business rules by applying generic depth-first search (DFS)-based techniques to the business rules; and modifying the business rules based on the identified faults as recites in independent claim 1, and further fails to teach similarly worded limitations in independent claims 9, 17, and 22, as pointed out in applicant's Remarks, pages 11-12.

These claimed limitations are not present in the prior art of record and would not have been obvious, thus all pending claims 1-2, 6-10, 14-23, and 27-28 are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

Art Unit: 2191

accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anna Deng whose telephone number is 571-272-5989. The examiner can normally be reached on Mondays to Fridays 9:30 - 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wei Zhen can be reached on 571-272-3708. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Anna Deng/

Primary Examiner, Art Unit 2191

11/26/2009